1A,50-1000V High Efficient Rectifiers

Features

- Low leakage current
- Low forward voltage drop
- Glass passivated chip junction
- For general purpose applications
- Moisture sensitivity: level 1, per J-STD-020
- For fast switching and low logic level applications
- High temperature soldering guaranteed: 260℃/10 seconds

DO-41(DO-204AL)/A-405

Applications

- Small battery charger, Power supplies

Maximum Ratings & Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	HER101	HER102	HER103	HER104	HER105	HER106	HER107	HER108	Unit
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	300	400	600	800	1000	V
Maximum average forward rectified current	$I_{F(AV)}$	1								A
Peak forward surge current,8.3ms single half sine-wave superimposed on rated load per diode	I_{FSM}	30								A
Operating junction temperature range	T_J	-55 to +135								°C
Storage temperature range	T_{STG}	-55 to +150								°C

Thermal-Mechanical Specifications (TA=25℃ unless otherwise noted)

Parameter	Symbol	Typ	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	63	°C /W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	30	°C /W
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	24	°C /W

Electrical Specifications (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Test Conditions	HER101	HER102	HER103	HER104	HER105	HER106	HER107	HER108	Unit
Forward Drop Voltage	V_F	I_F=1A	1.0			1.3		1.7			V
Reverse leakage current @V_R	I_R	T_J =25℃	5								uA
		T_J =125℃	100								
Typical junction capacitance	C_J	4.0 V 1 MHZ	20					15			pF
Maximum reverse recovery time	trr	I_F=0.5A, I_R=1.0A, I_{RR}=0.25A	50					75			nS

Note：

1. Valid provided that leads at a distance of 9.5 mm from case are kept at ambient temperature.

Ratings and Characteristics Curves

(TA = 25℃ unless otherwise noted)

FIG.1-MAXIMUM FORWARD CURRENT DERATING CURVE

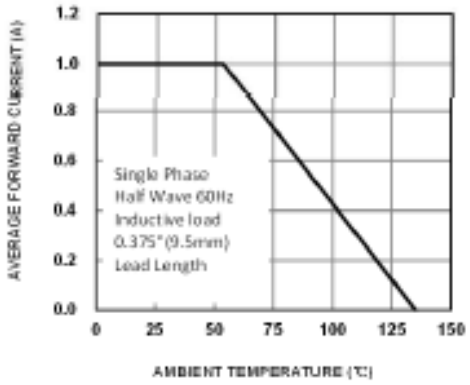


FIG.2-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

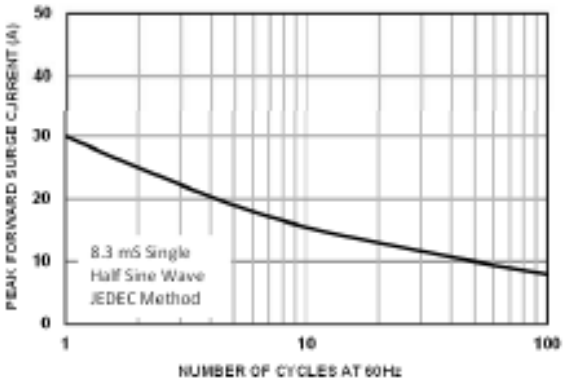


FIG.3-TYPICAL FORWARD CHARACTERISTICS

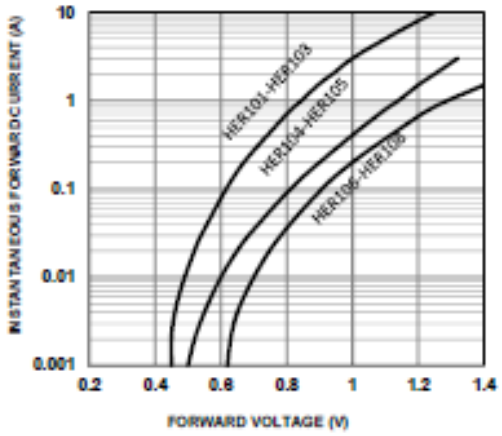


FIG.5-TYPICAL JUNCTION CAPACITANCE

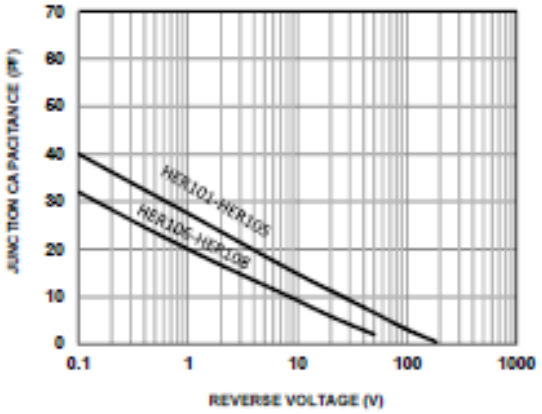


FIG.4-TYPICAL REVERSE CHARACTERISTICS

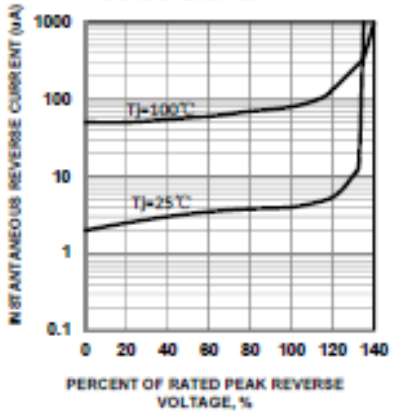
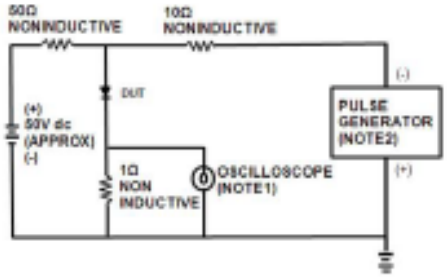
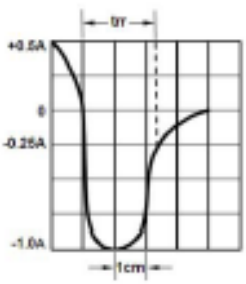


FIG .5 - REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



NOTES: 1. Rise Time=7ns max. Input Impedance= 1 megohm 22pf
2. Rise Time=10ns max. Sourse Impedance= 50 ohms

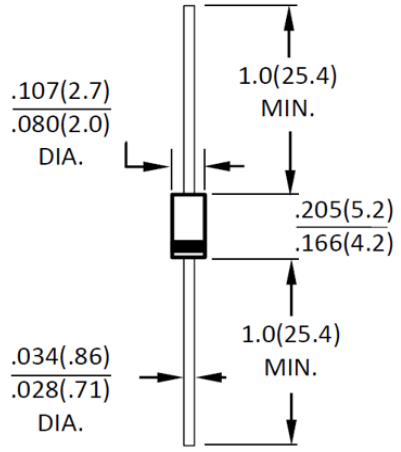


SET TIME BASE FOR 5/10ns/cm

Package Outline Dimensions

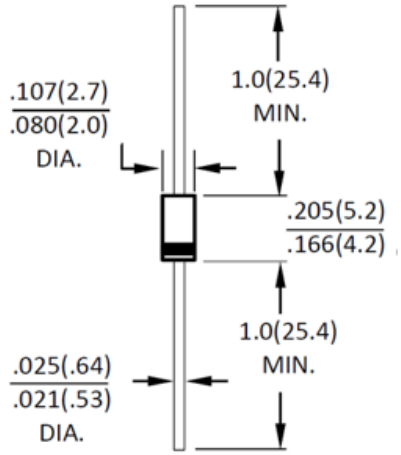
in inches (millimeters)

DO-41(DO-204AL)



Dimensions in inches and (millimeters)

A-405



Dimensions in inches and (millimeters)

Revision History

Document Version	Date of release	Description of changes
Rev.A	2021.06.01	Released Datasheet
Rev.B	2024.02.23	Modify document format

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